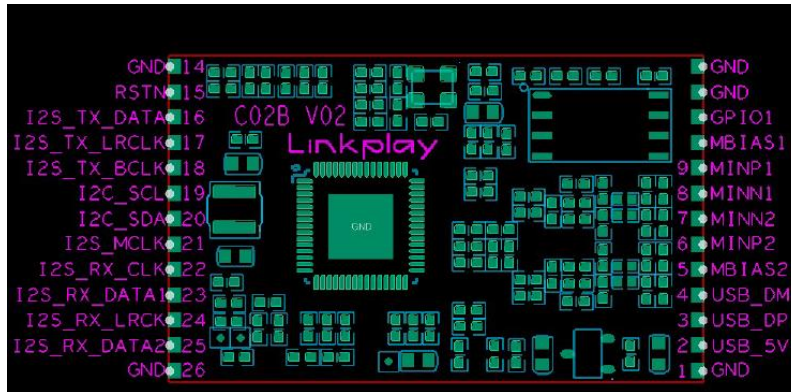


LinkPlay C02B Module Pin Out



PIN	PIN NAME	TYPE	DESCRIPTION
1, 12, 13, 14, 26	GND	Power	Digital ground
2	USB_5V	Power	Input >100 mA
3	USB_DP	I/O	Full speed USB 2.0
4	USB_DM	I/O	
5	MBIAS2	O	MIC2 bias voltage
6	MINP2	I	MIC2 in positive
7	MINN1	I	MIC2 in negative
8	MINN1	I	MIC1 in negative
9	MINP1	I	MIC1 in positive
10	MBIAS1	O	MIC1 bias voltage
11	GPIO1	O	General purpose input output, wake-up output
15	RSTN	I, IPU	Module reset, built-in RC reset, firmware upgrade
16	I2S_TX_DATA	O	I2S TX signal (reversed)
17	I2S_TX_LRCLK	I/O	
18	I2S_TX_BCLK	I/O	
19	I2C_SCL	I/O	I2C port, pull up with 1.8 kohm resistor, firmware upgrades with RSTN
20	I2C_SDA	I/O	
21	I2S_MCLK	O	I2S master clock
22	I2S_RX_CLK	I/O	I2S RX input output bit clock
23	I2S_RX_DATA1	I	AEC RXI2C input data reference for 2 channels
24	I2S_RX_LRCK	I/O	I2S RX input output L/R clock
25	I2S_RX_DATA2	I	AEC I2S RX input data reference for 4 channels

Notes:

- I: Input
- O: Output
- P: Power
- IPU: Internal Pull Up
- IPD: Internal Pull Down