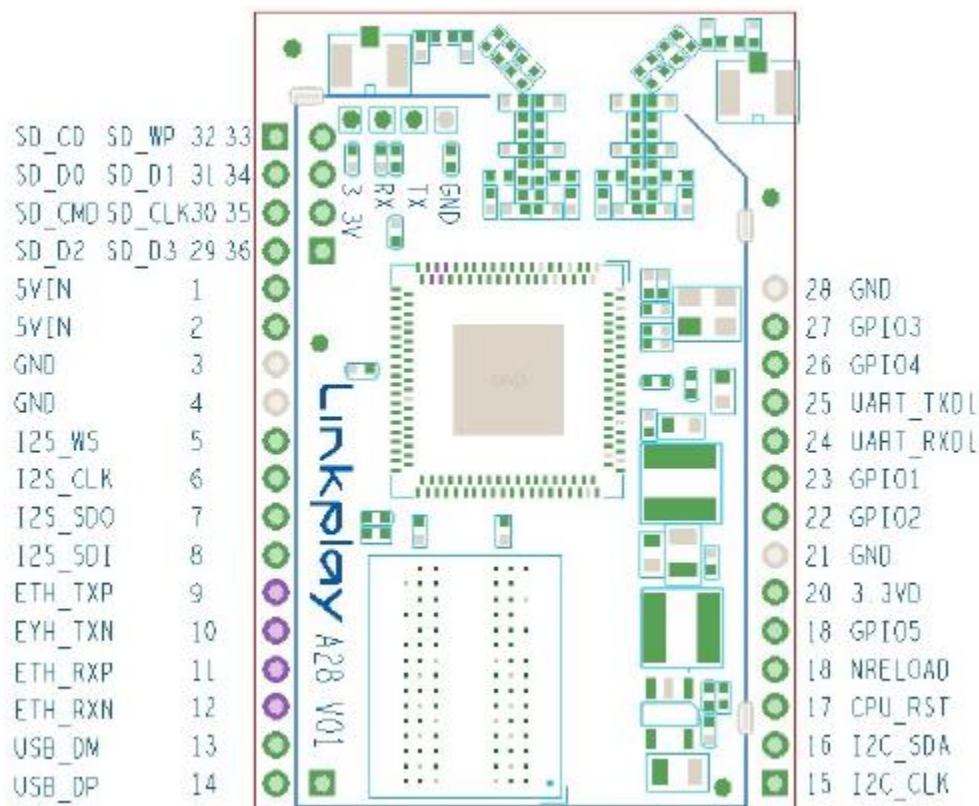


LinkPlay A28 Module Pin Out



PIN	DESCRIPTION	NET	TYPE	COMMENTS
3, 4, 21, 28	GND	GND	Power	Ground
1, 2	5VD Input	DC+5V	Power, I	+5V@350mA
20	3.3VD Output	DC+3.3V	Power, O	Output +3.3V@100mA
5	I2S_WS	I2S_WS	O	All pins could be pin shared as GPIO
6	I2S_CLK	I2S_CLK	I/O	
7	I2S_SDO	I2S_SDO	O	
8	I2S_SDI	I2S_SDI	I	
9	ETH_TXP	ETH_TXP	O	10M/100M Ethernet interface (current driven)
10	ETH_TXN	ETH_TXN	O	
11	ETH_RXP	ETH_RXP	I	
12	ETH_RXN	ETH_RXN	I	
13	USB_DM	USB_DM	I/O	USB 2.0 host
14	USB_DP	USB_DP	I/O	
15	I2C_CLK	I2S_CLK	O	I2C interface, in module 4.7 kohm resistor pull-up
16	I2C_SDA	I2C_SDA	I/O	
17	Module reset	CPU_RST	I, IPU	Reset with pull down, reset time >300ms
18	Restore factory settings	nReload	I/O, IPU	First input 0 and keep for more than 5 seconds then change to 1, it will go back to

				factory setting, in module 4.7 kohm resistor pull-up
19	GPIO	GPIO5	I/O, IPU	In module 4.7 kohm resistor pull-up
22	GPIO	GPIO2	I/O, IPU	In module 4.7 kohm resistor pull-up
23	GPIO	GPIO1	I/O, IPU	In module 4.7 kohm resistor pull-up
24	UART receive	UART_RXD	I	Need keep UART_TXD high during chip boot-up
25	UART transmit	UART_TXD	O, IPU	
26	GPIO	GPIO4	I/O, IPU	
27	GPIO	GPIO3	I/O, IPU	
29	SDIO Data	SD_D2	I/O	SDIO, GPIO 10 kohm pull-up
30	SDIO CMD	SD_CMD	O	SDIO CMD, GPIO 10 kohm pull-up
31	SDIO Data	SD_D0	I/O	SDIO Data0, GPIO 10 kohm pull-up
32	SDIO Detect	SD_DC	I	SDIO detect, GPIO, 10 kohm pull-up
33	SDIO WP	SD_WP	I	Write protect, GPIO, 10 kohm pull-up, If MICRO SD (TF), need put pin to GND
34	SDIO Data	SD_D1	I/O	SDIO Data 1, GPIO, 10 kohm pull-up
35	SDIO CLK	SD_CLK	O	SDIO CLK, GPIO
36	SDIO Data	SD_D3	I/O	SDIO Data 3, GPIO, 10 kohm pull-up
37	Reference CLK	REF_CLK	O, IPD	Reference clock output

Notes:

- I: Input
- O: Output
- P: Power
- IPU: Internal Pull Up
- IPD: Internal Pull Down
- A: Analog